FIG. 1 (PRIOR ART)

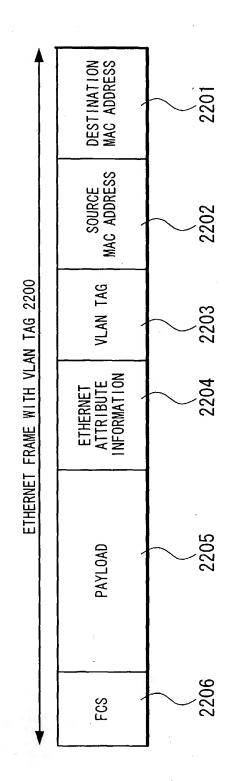


FIG. 2

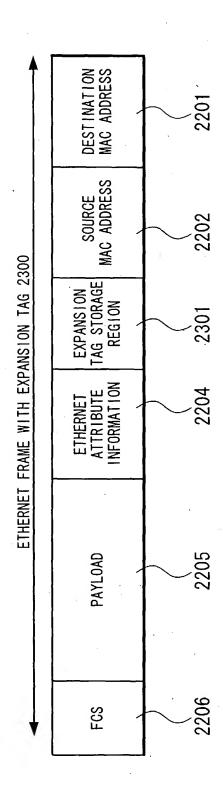
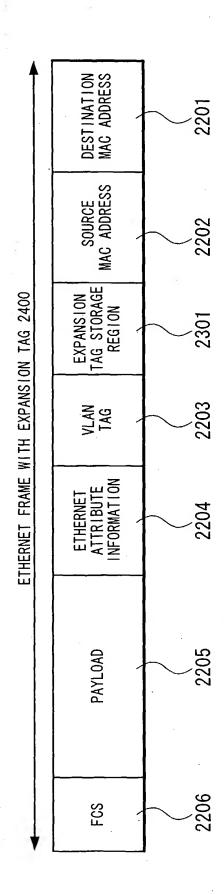
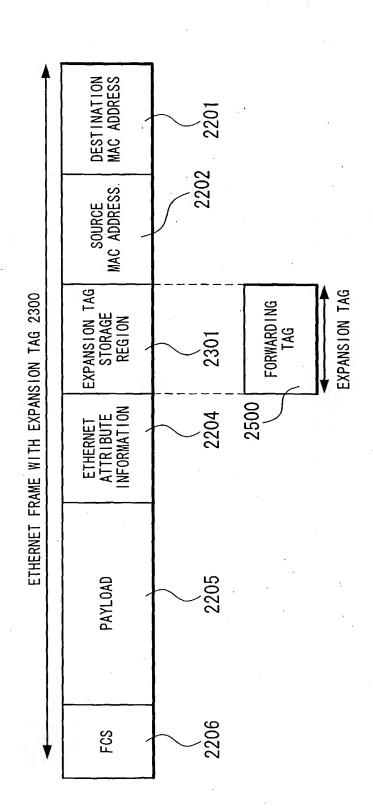


FIG. 3



a

FIG. 4



'n

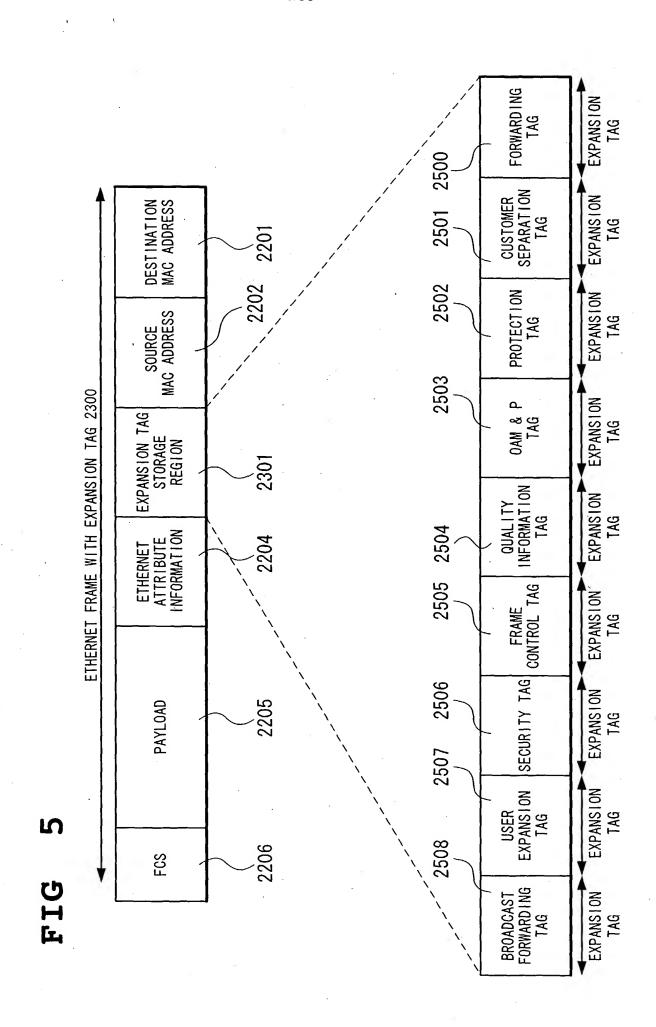
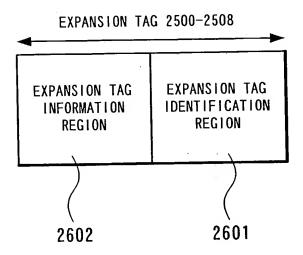


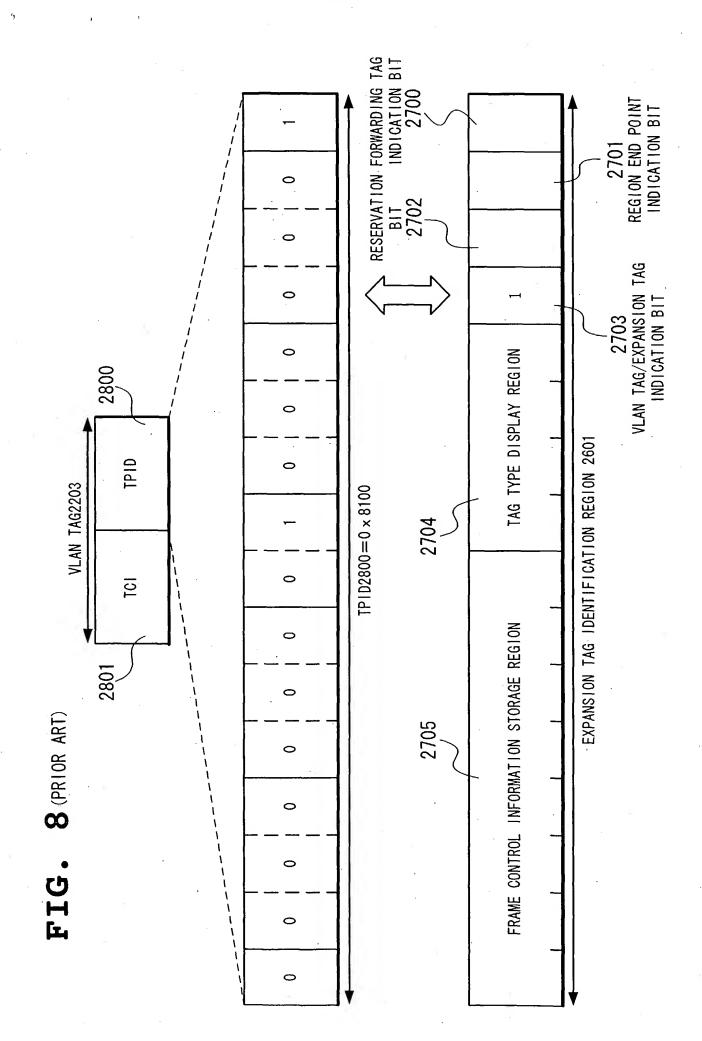
FIG. 6

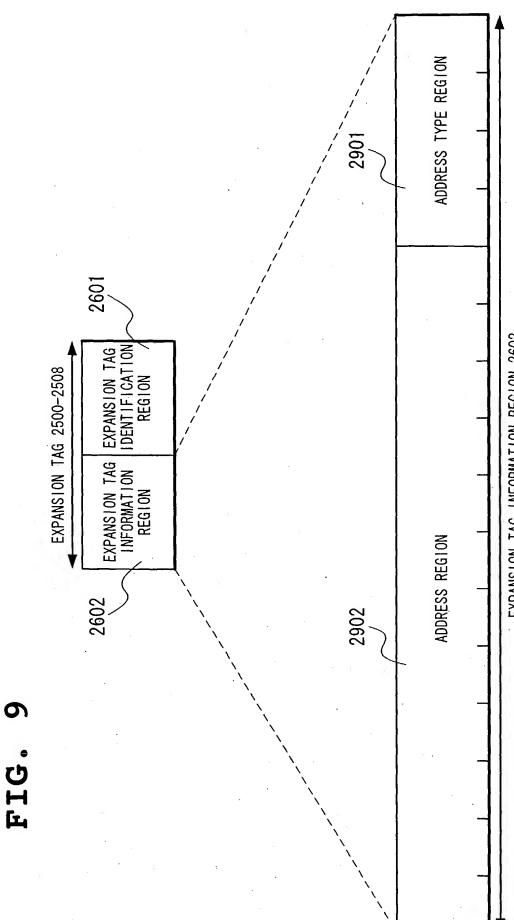


FORWARDING TAG INDICATION BIT 2700

2701 REGION END POINT INDICATION BIT

RESERVATION BIT 2702 2703 VLAN TAG/EXPANSION TAG INDICATION BIT TAG TYPE DISPLAY REGION 2601 EXPANSION TAG IDENTIFICATION REGION 2601 EXPANSION TAG IDENTIFICATION REGION EXPANSION TAG 2500-2508 EXPANSION TAG | INFORMATION | I REGION FRAME CONTROL INFORMATION STORAGE REGION 2602 2705 FIG.





EXPANSION TAG INFORMATION REGION 2602

FIG. 10

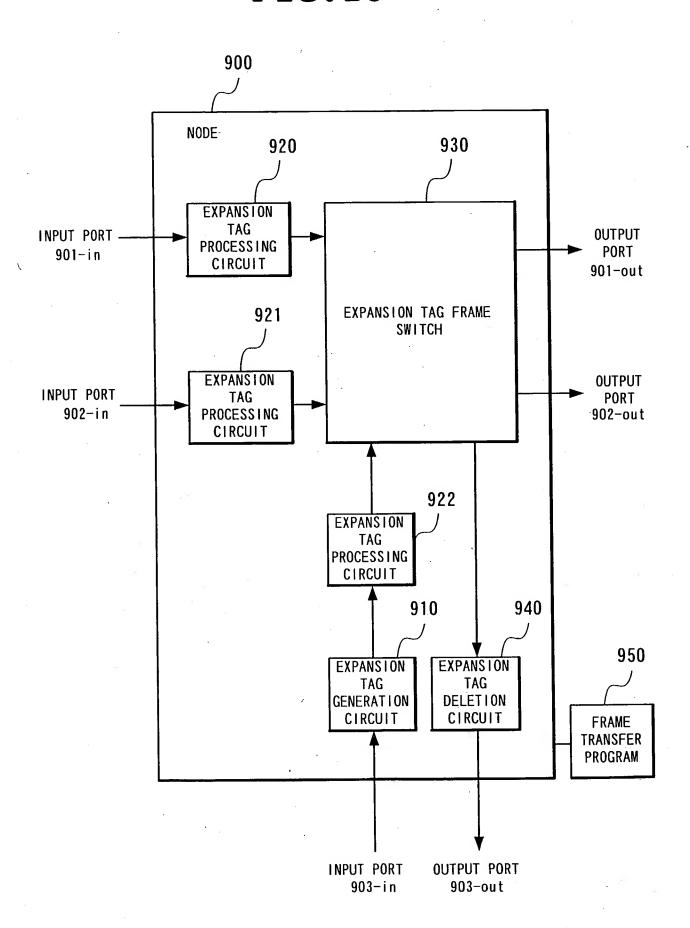
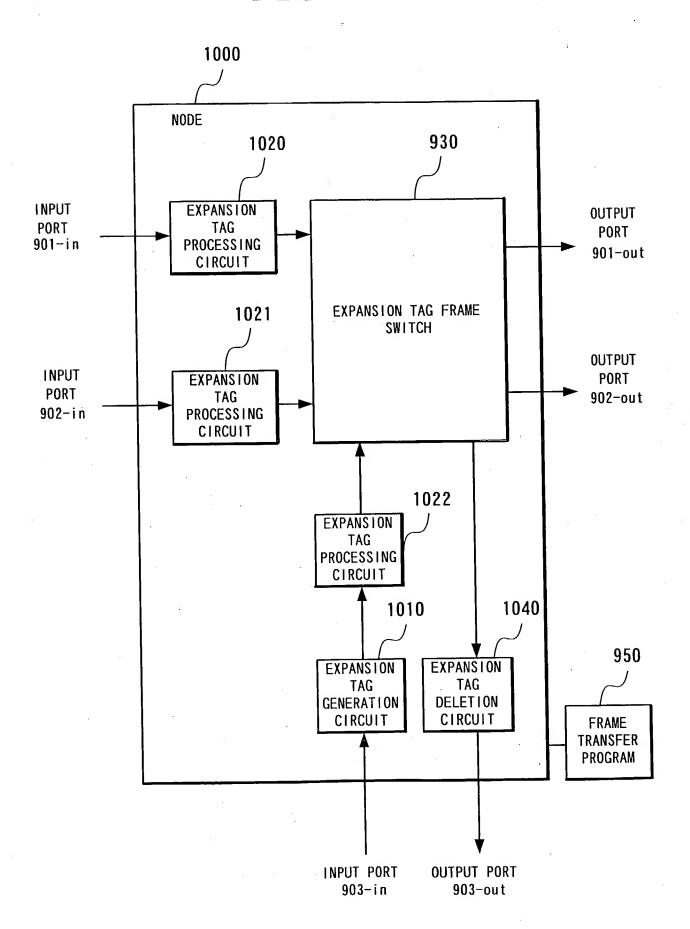
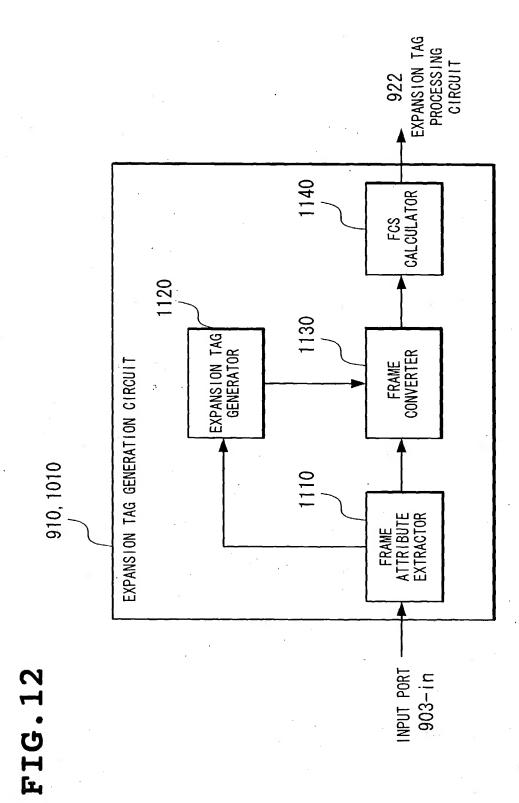


FIG.11





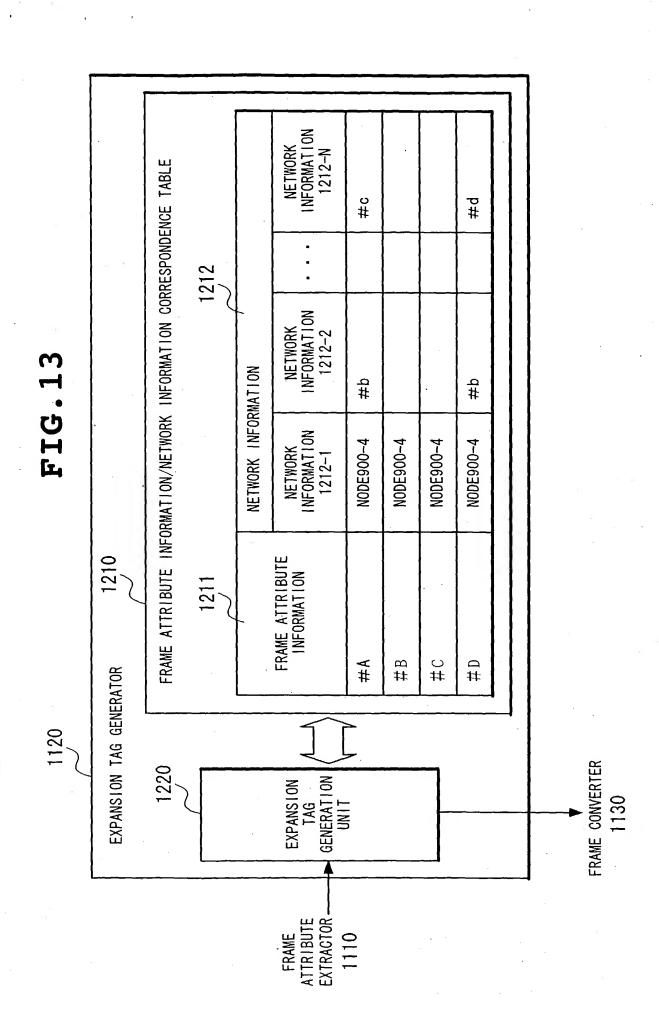
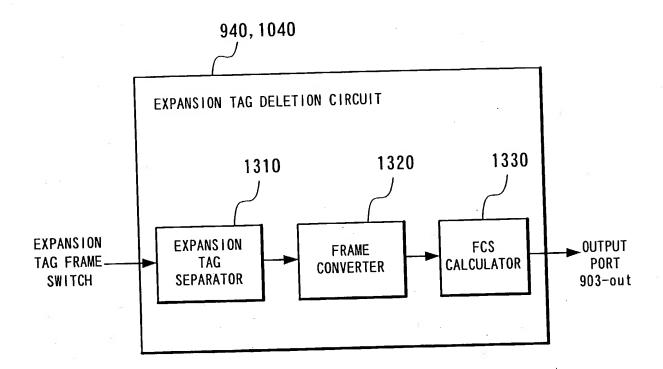
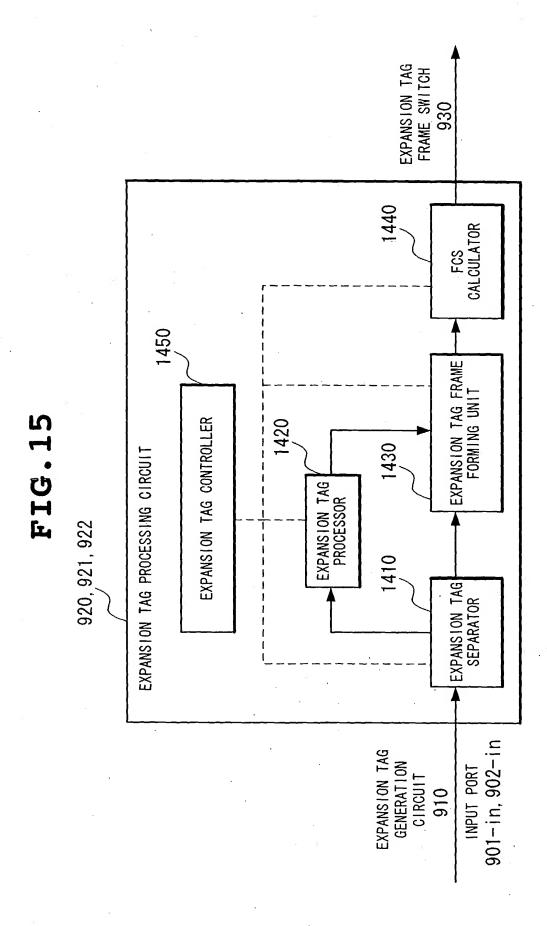
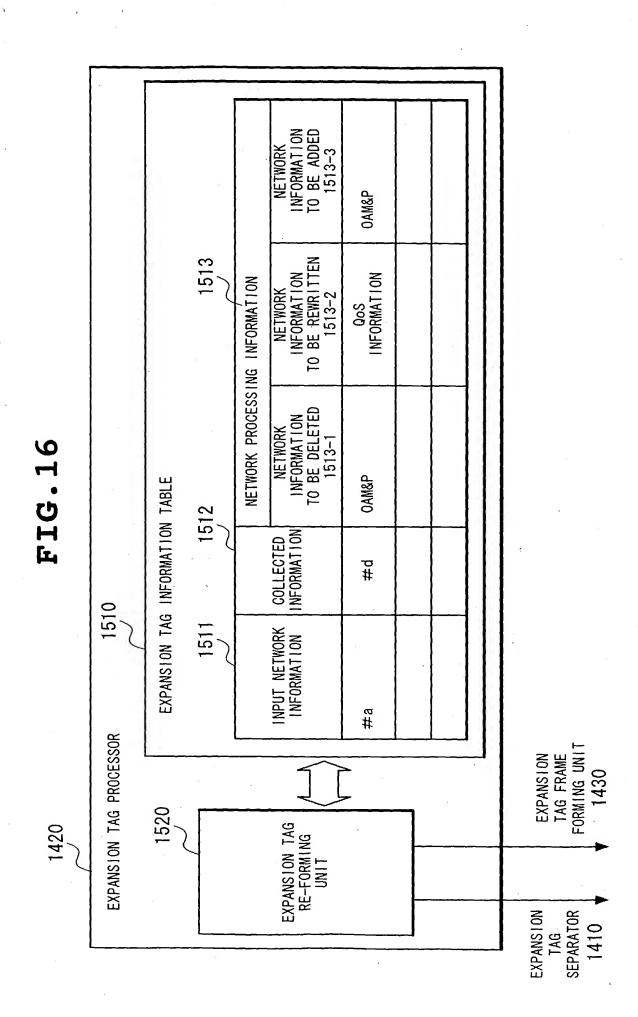


FIG. 14







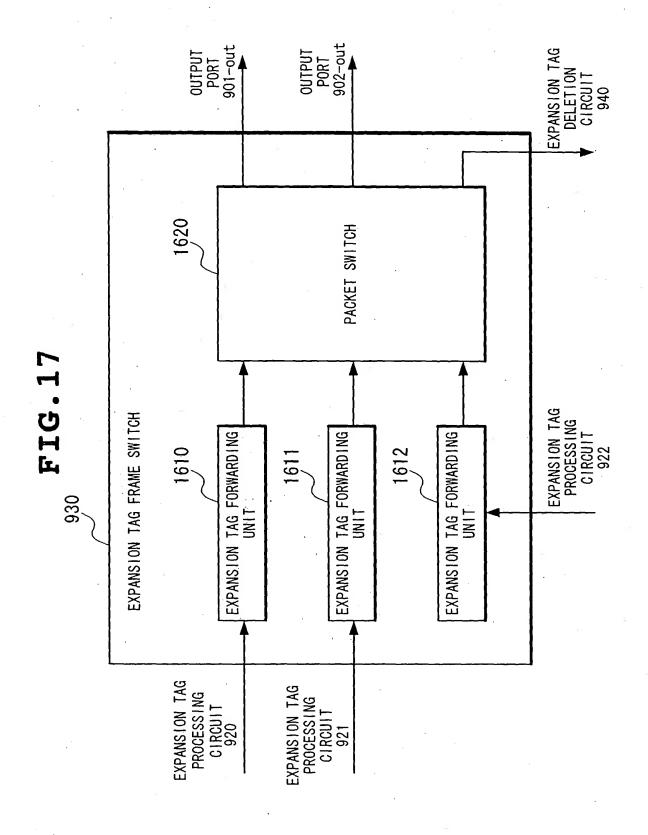


FIG. 18

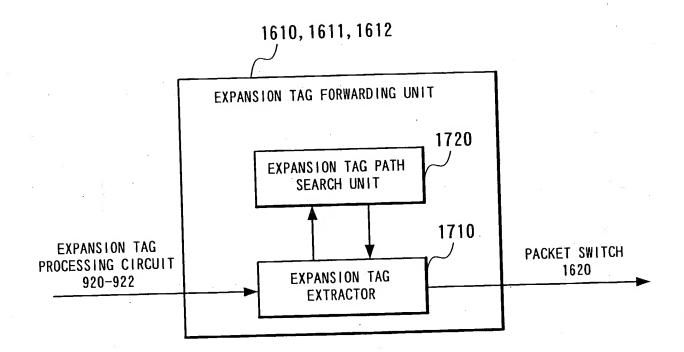
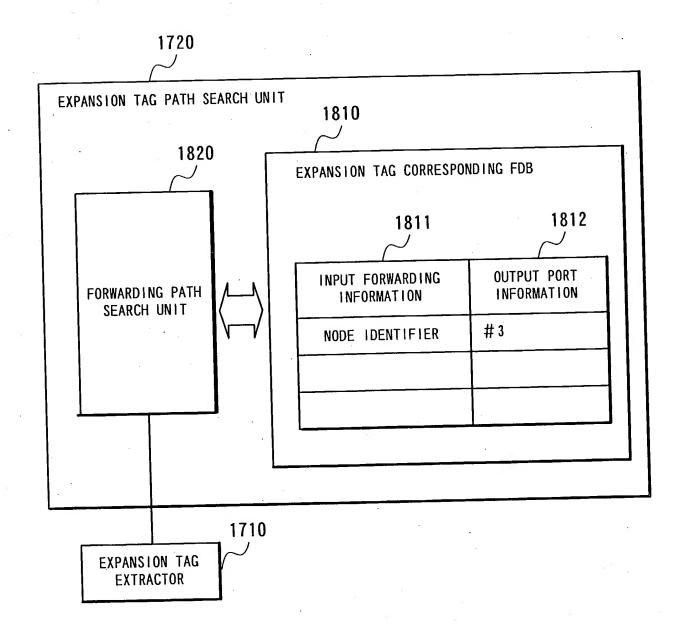
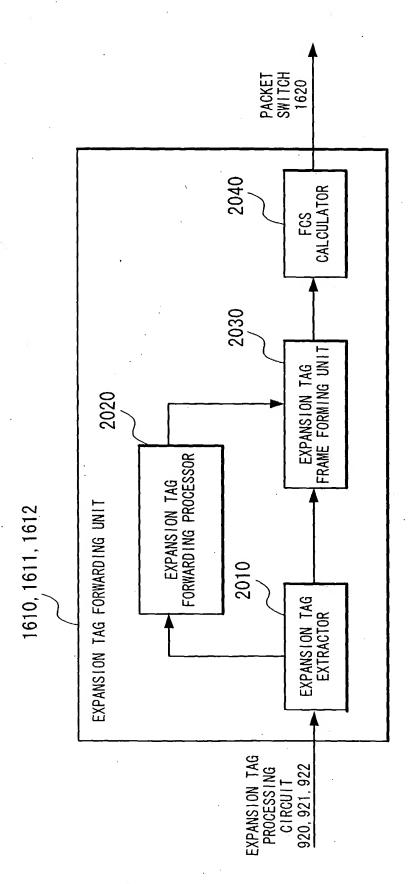
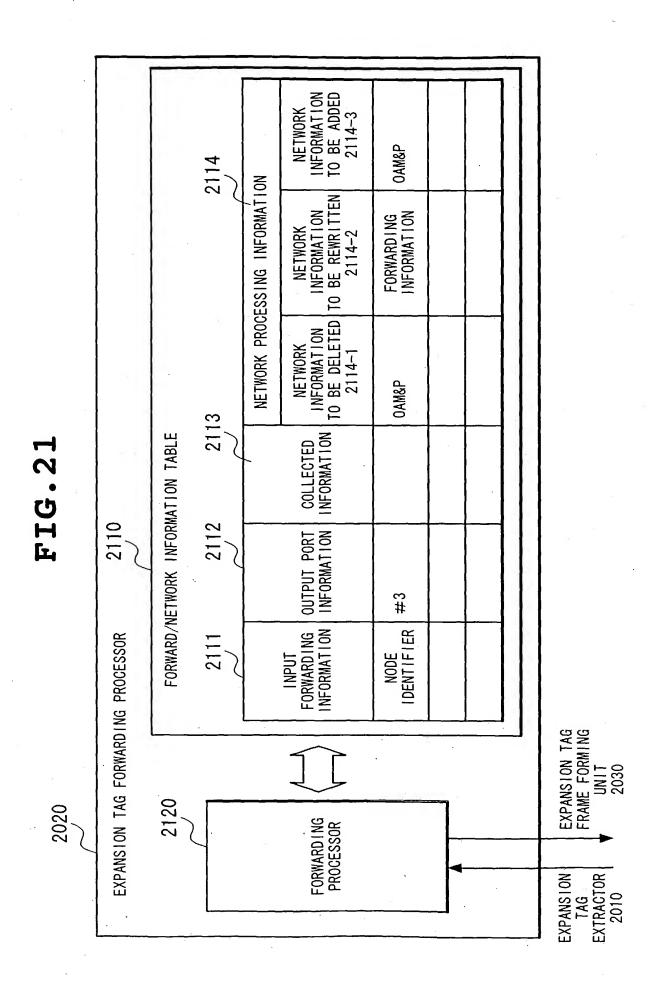


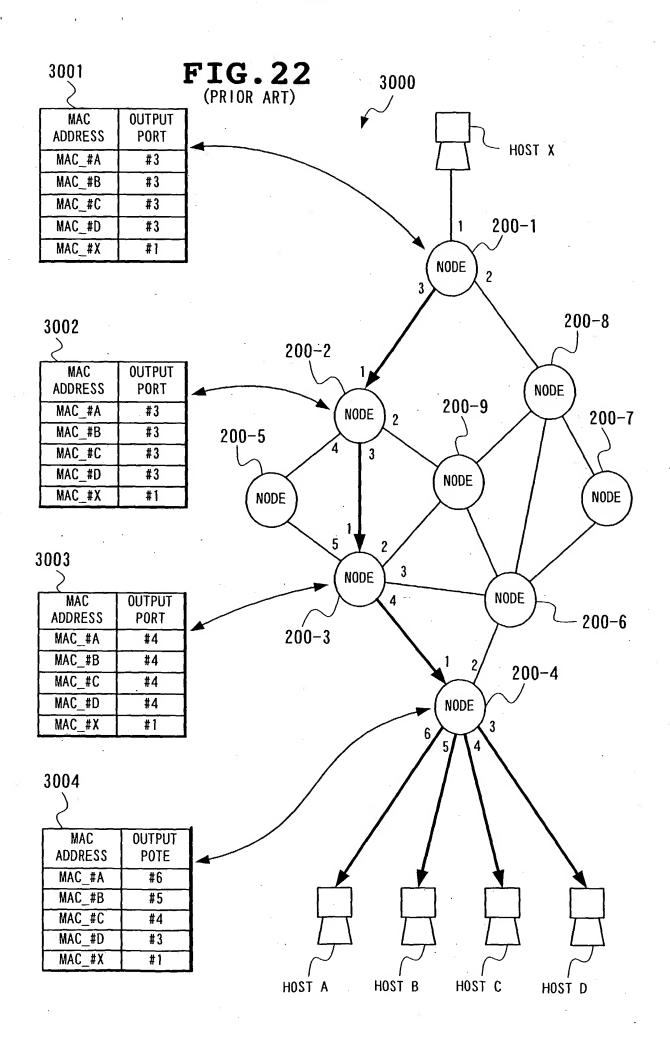
FIG. 19

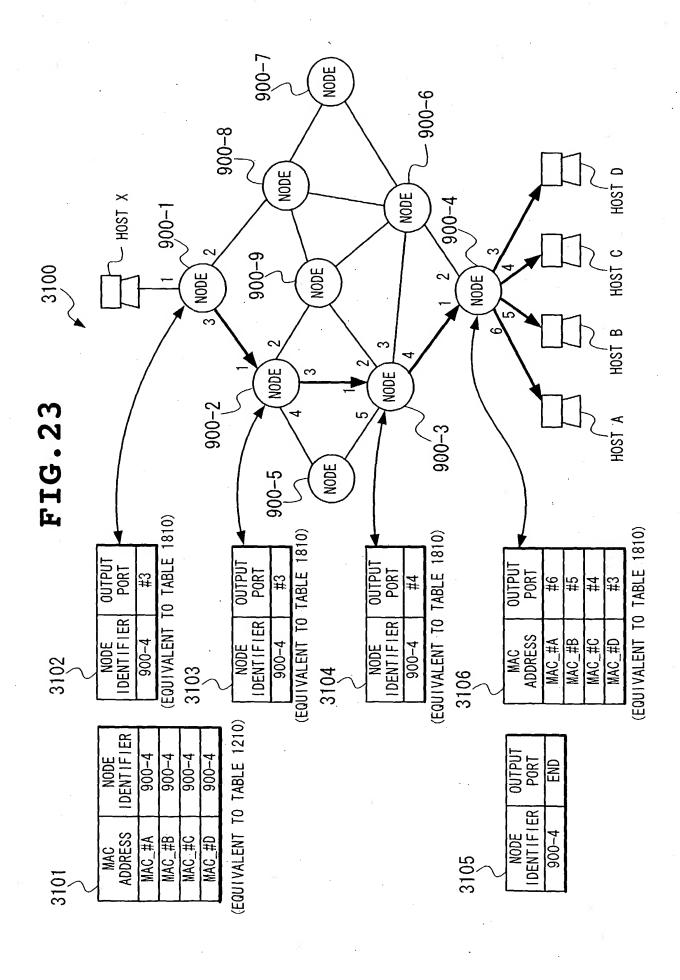


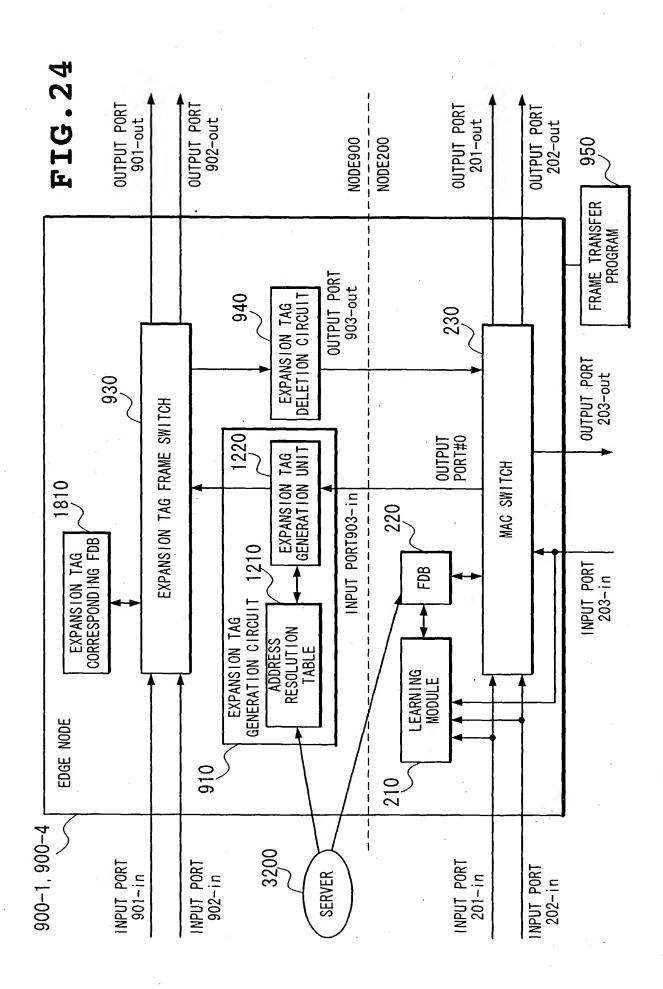












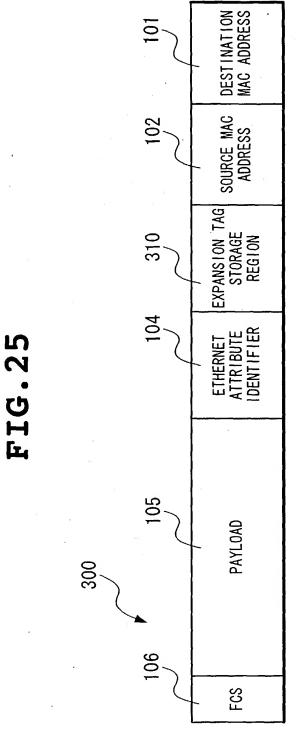


FIG. 26

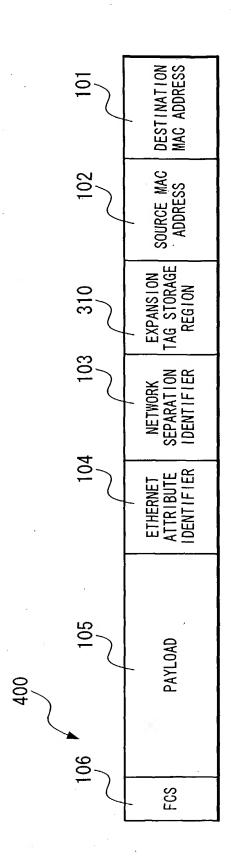
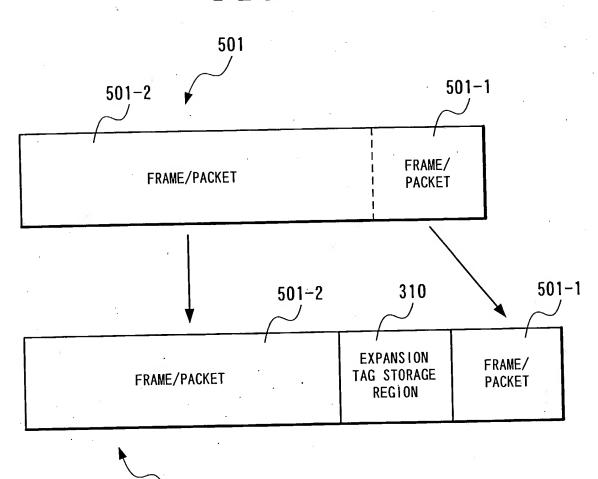


FIG.27



500

FIG. 28

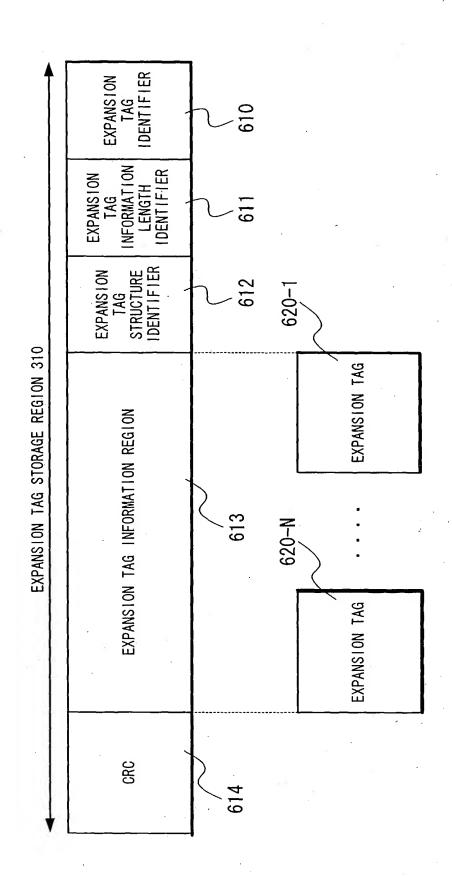


FIG. 29

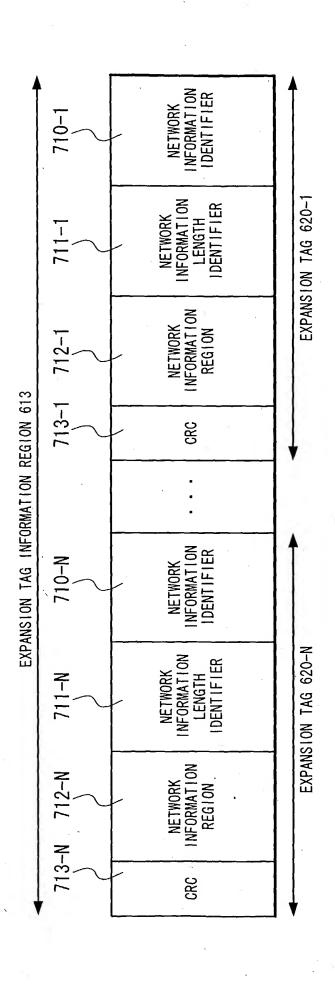
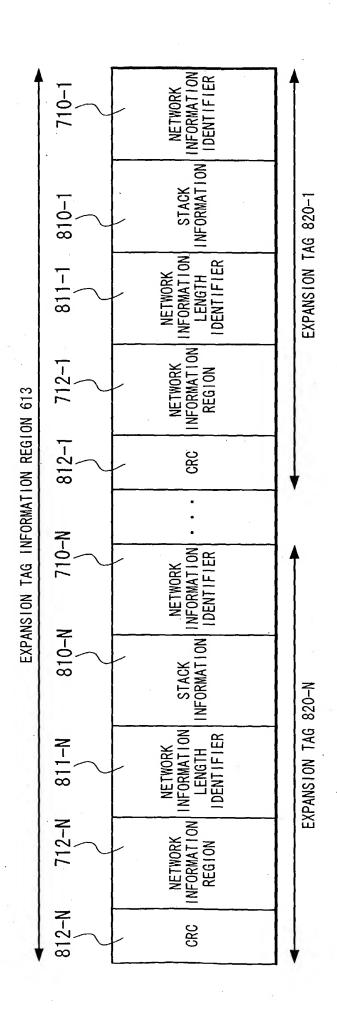


FIG. 30



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FIG. 31 (PRIOR ART)

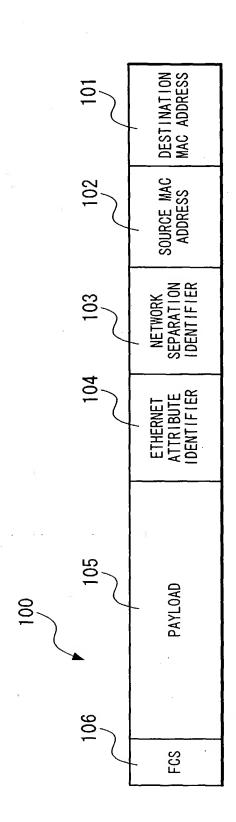


FIG. 32 (PRIOR ART)

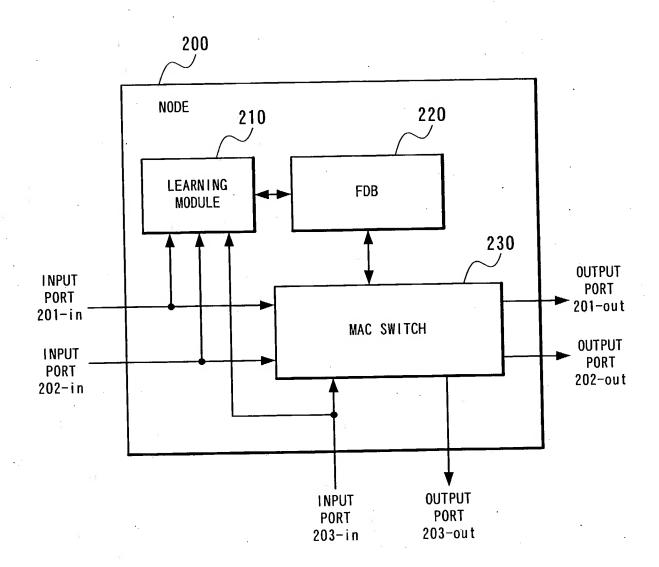


FIG.33

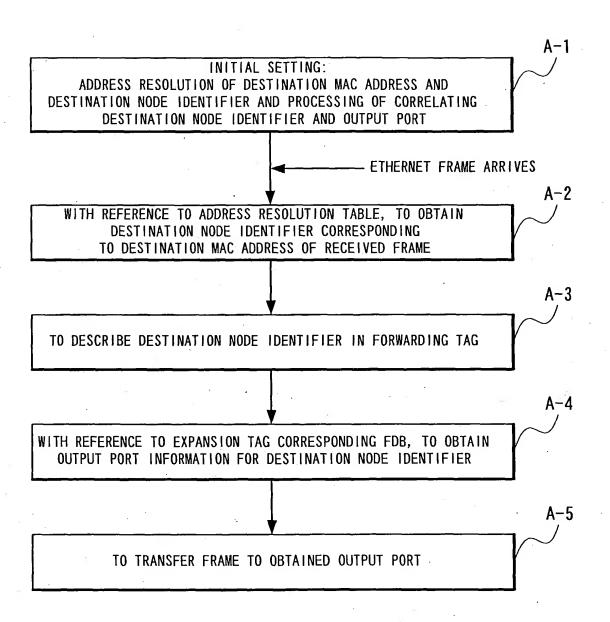


FIG.34

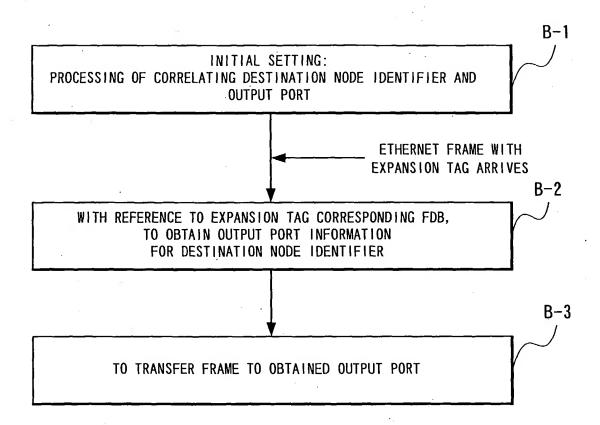
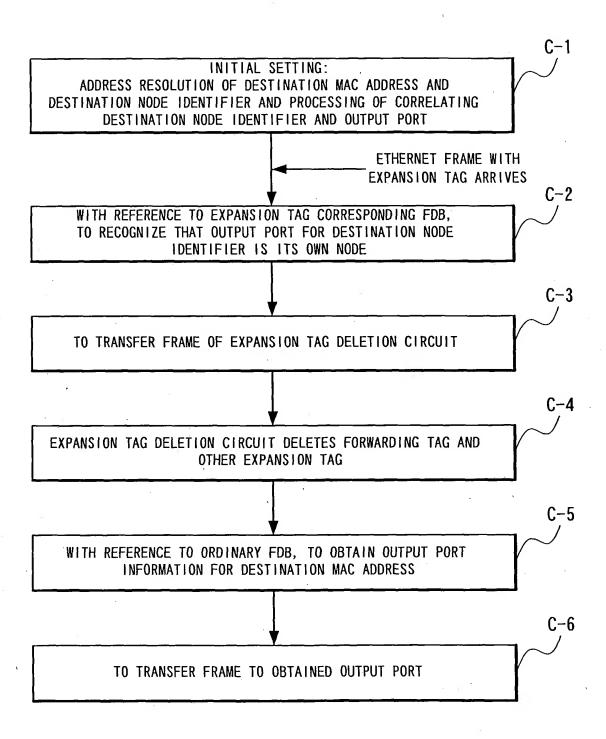
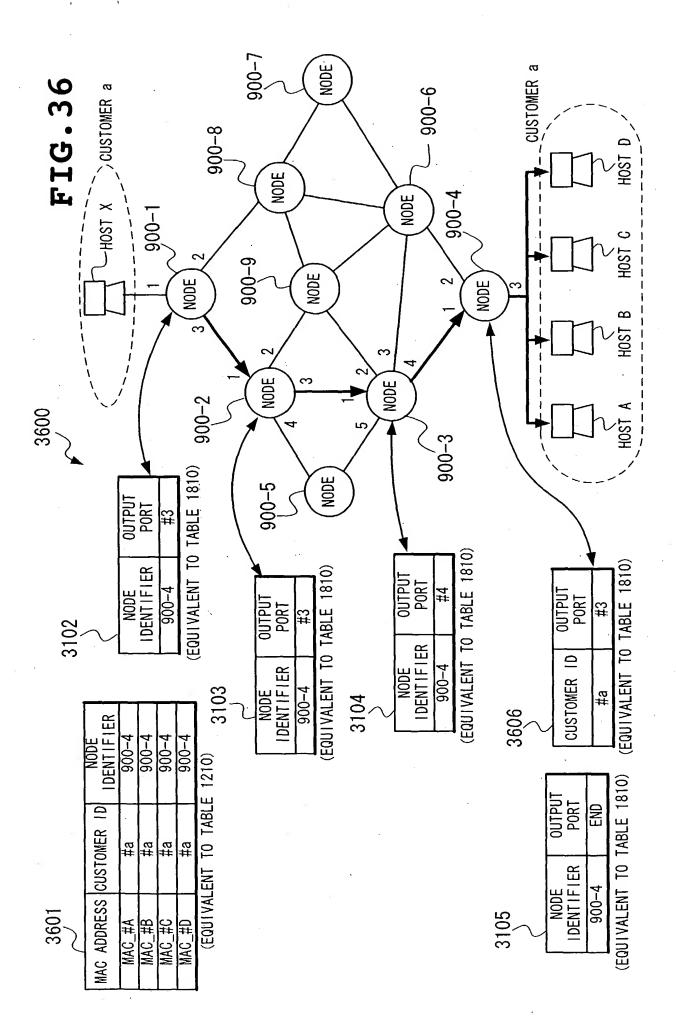
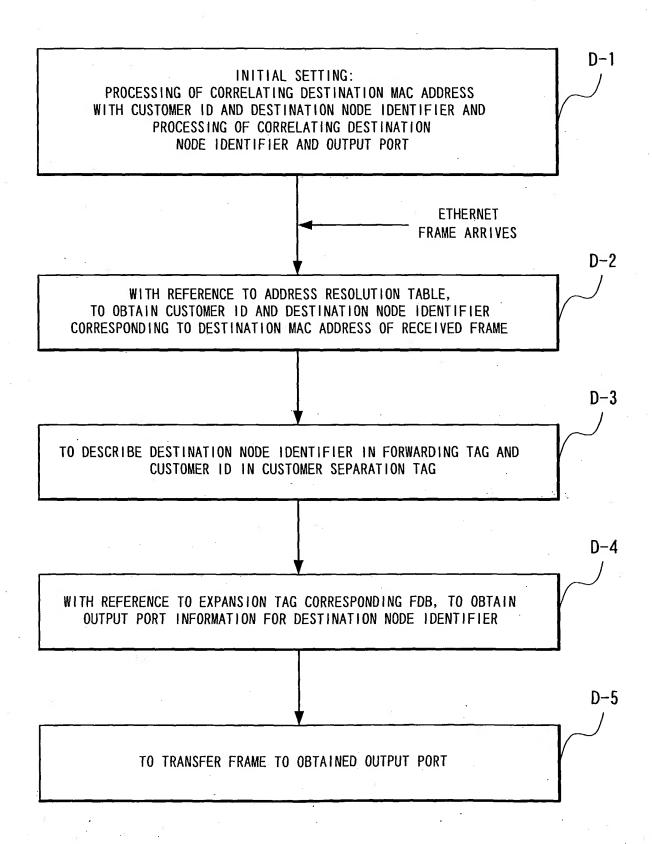


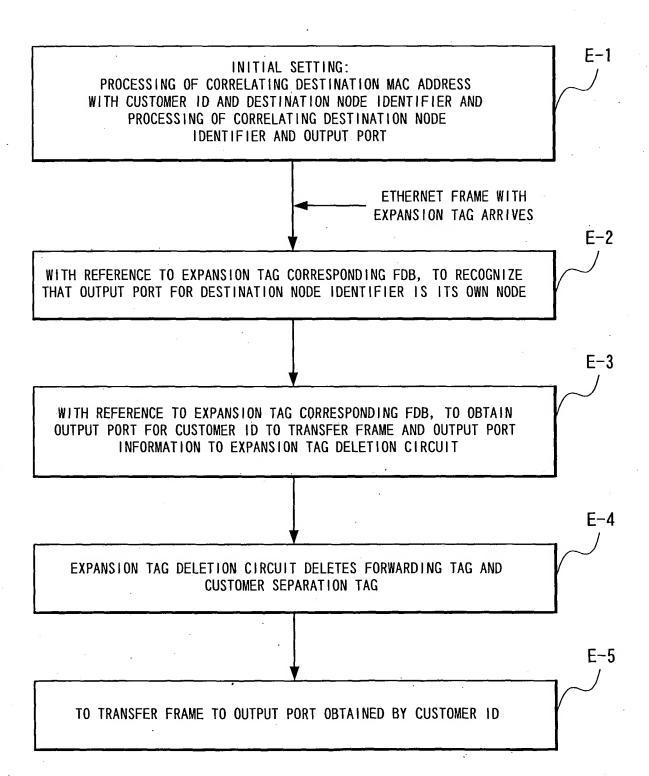
FIG.35

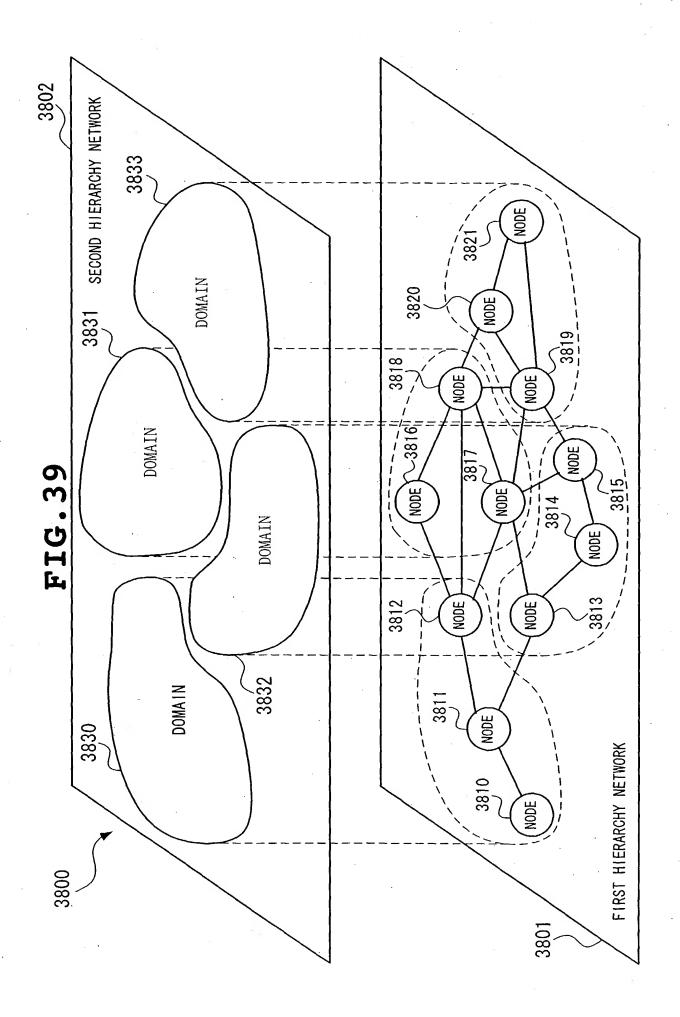






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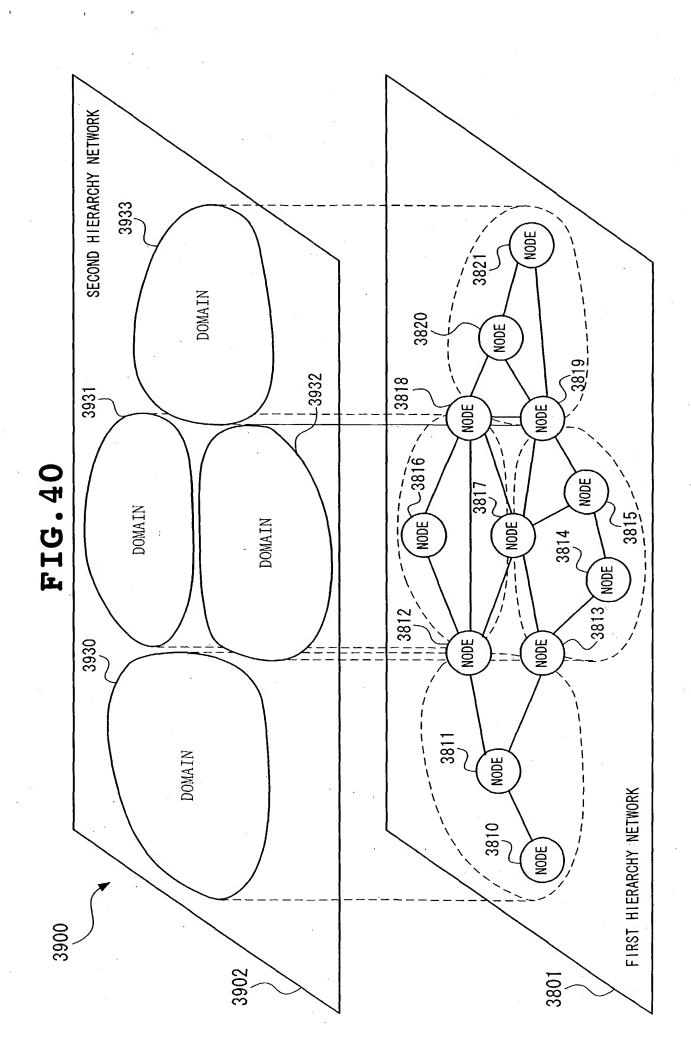
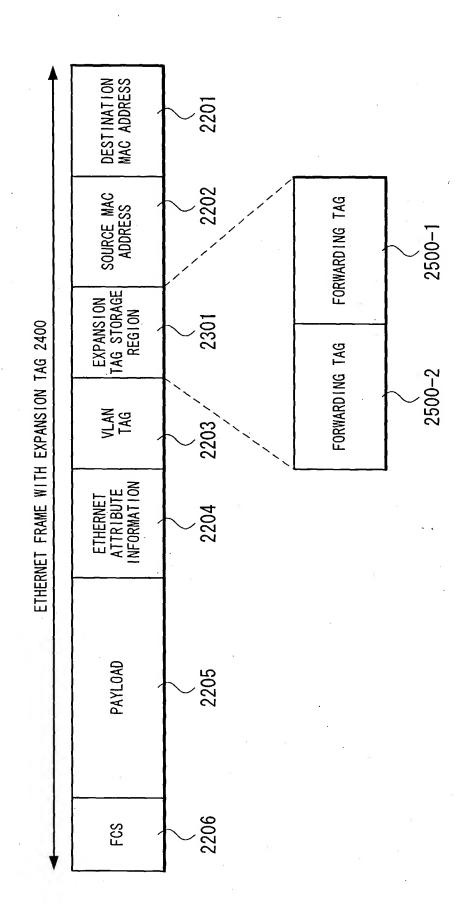
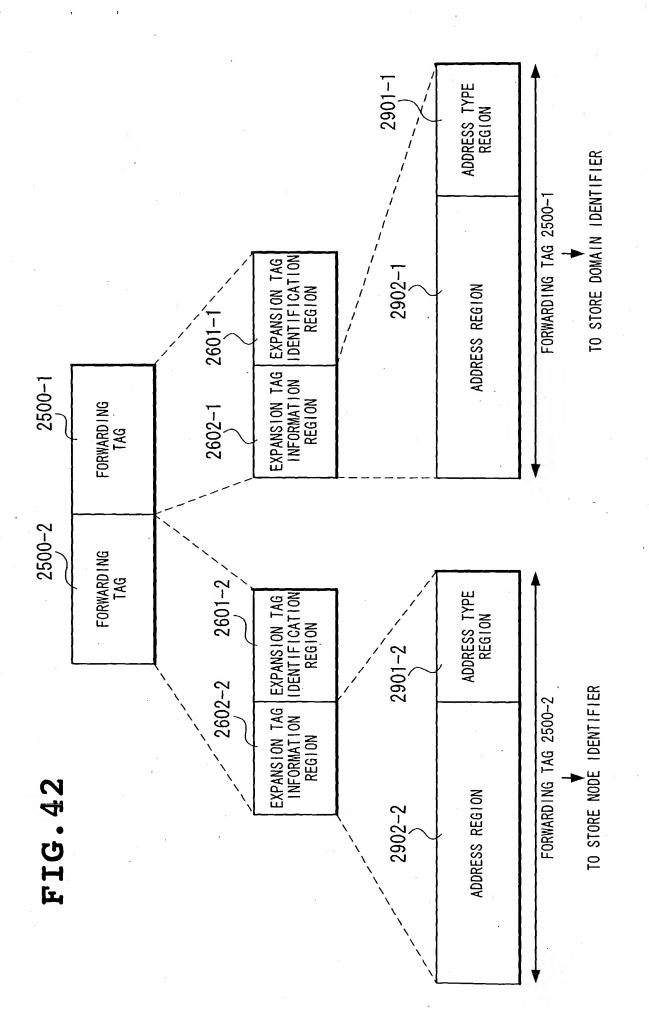
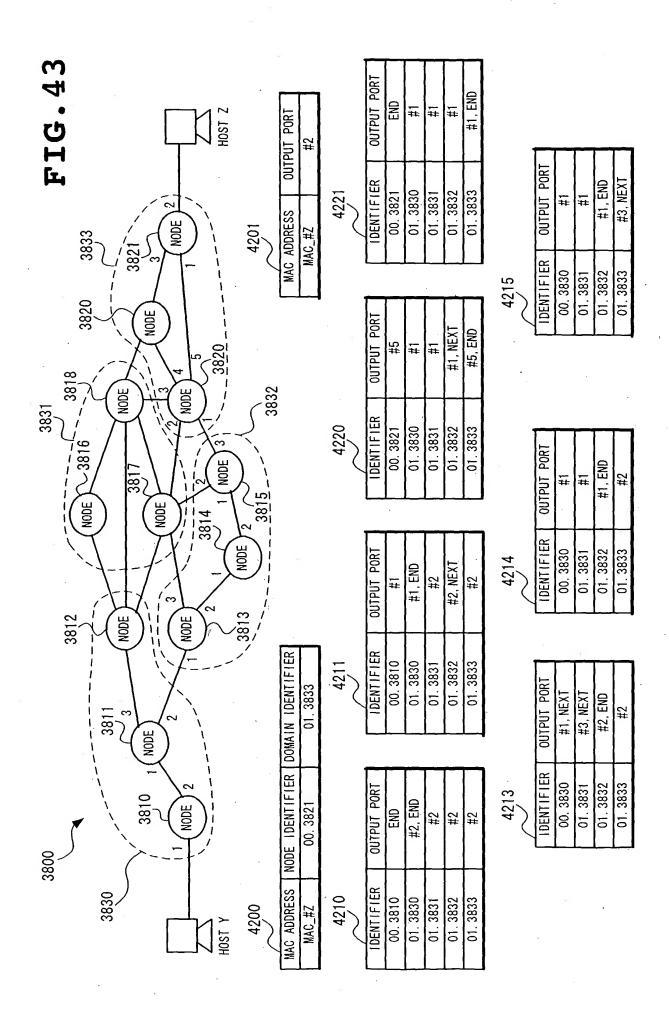


FIG. 41







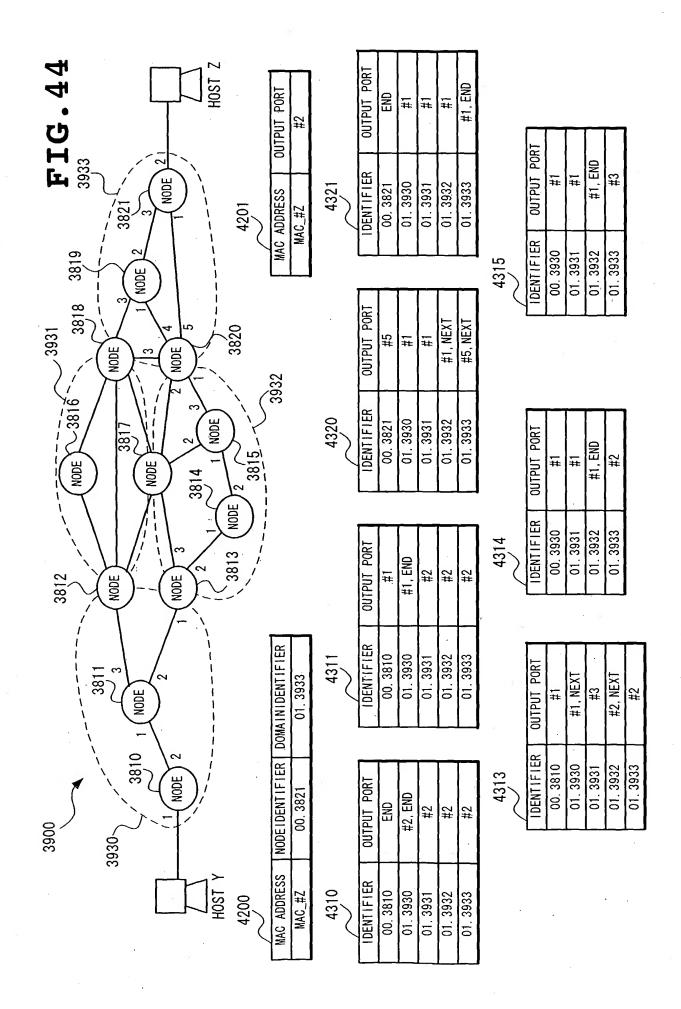
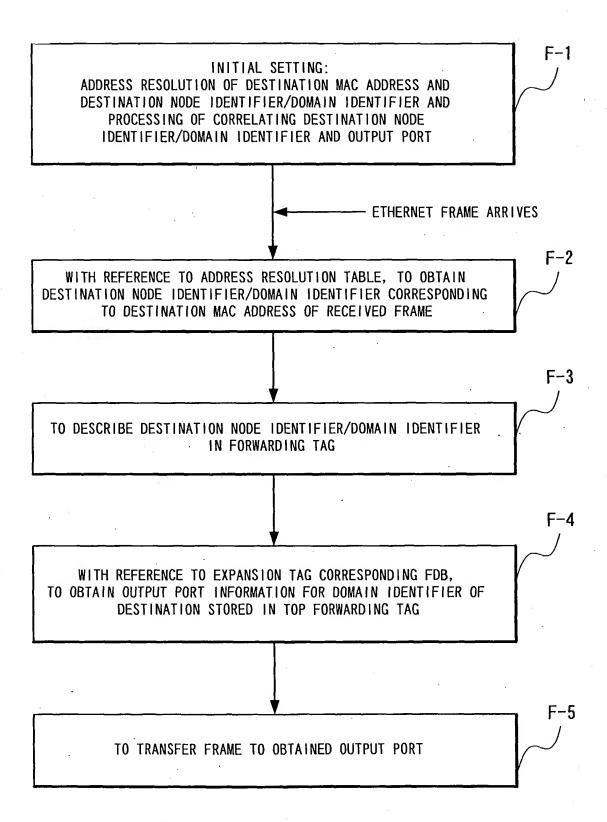
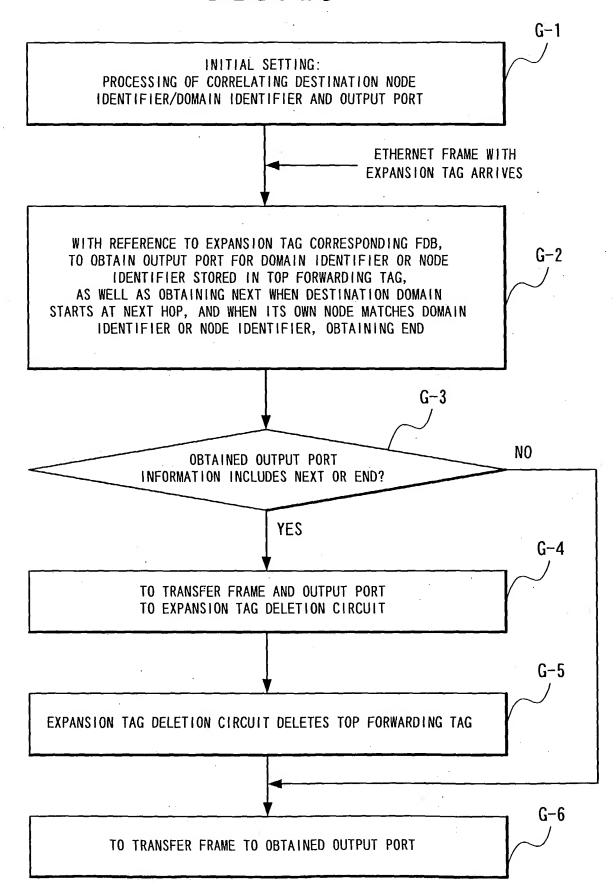
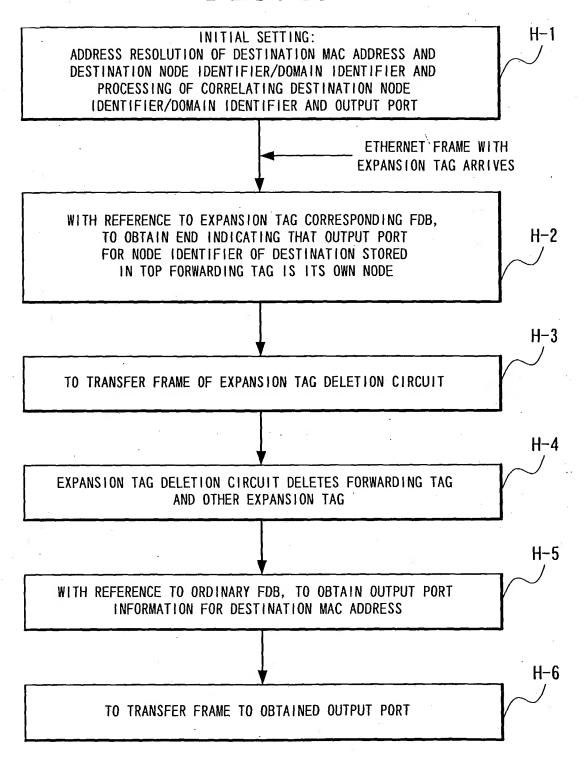


FIG 45







(A)

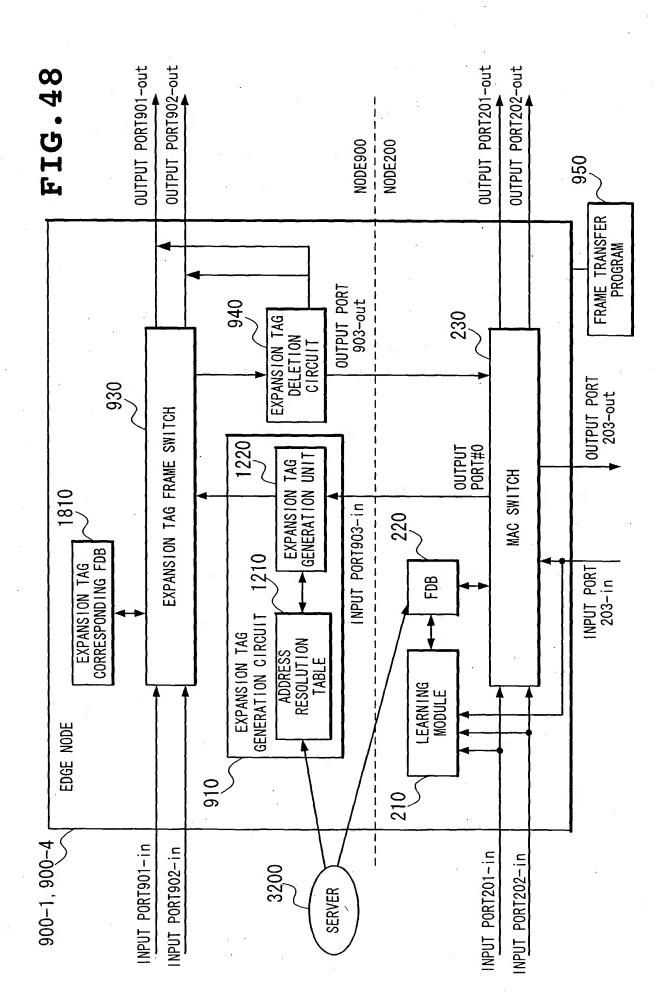


FIG. 49

100

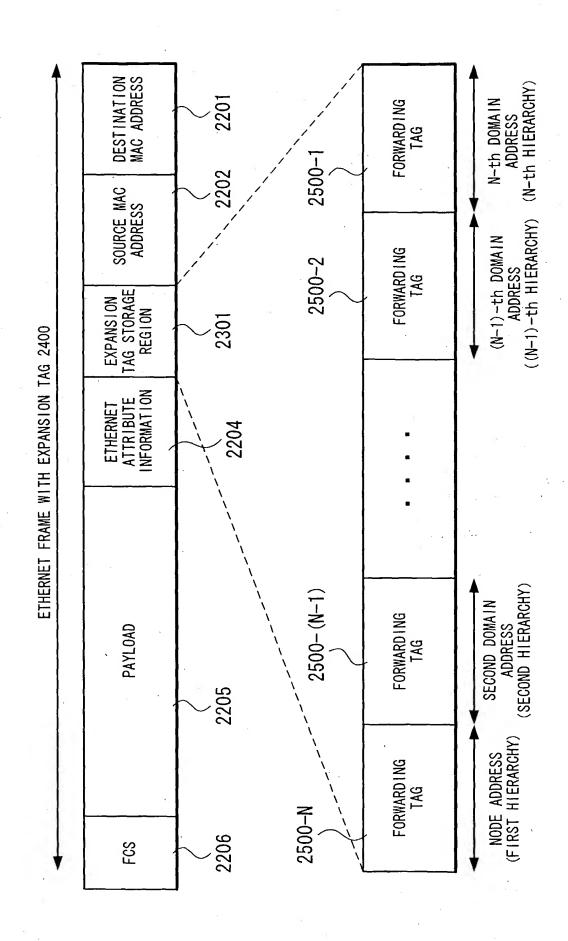
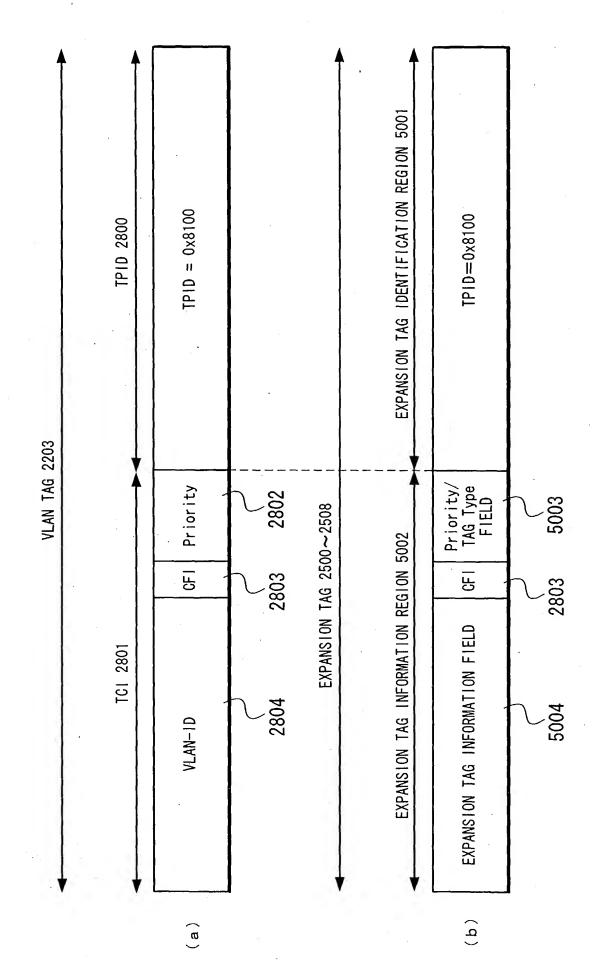
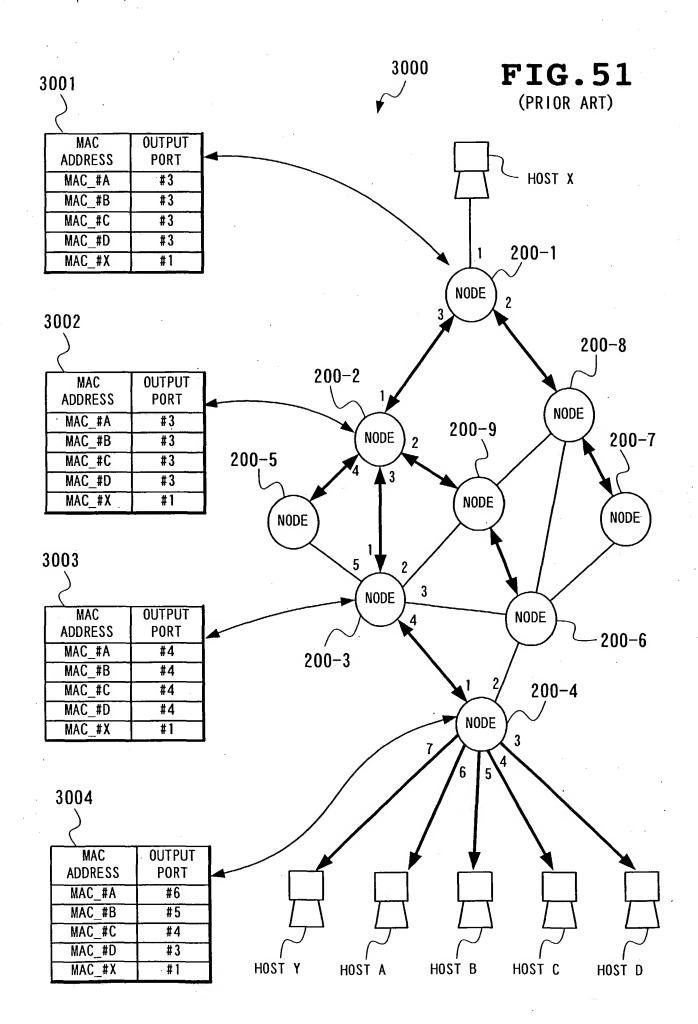
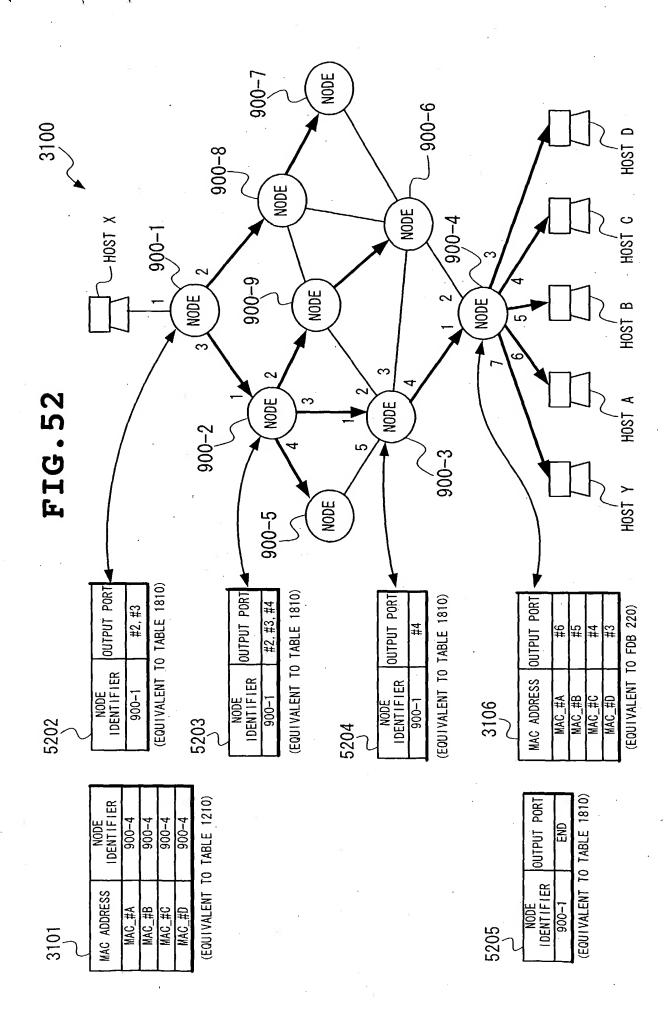


FIG. 50

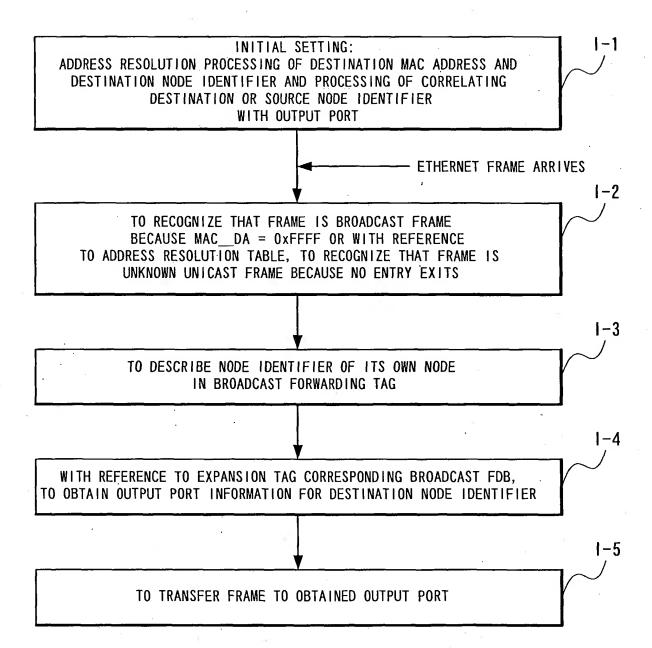
· (X)

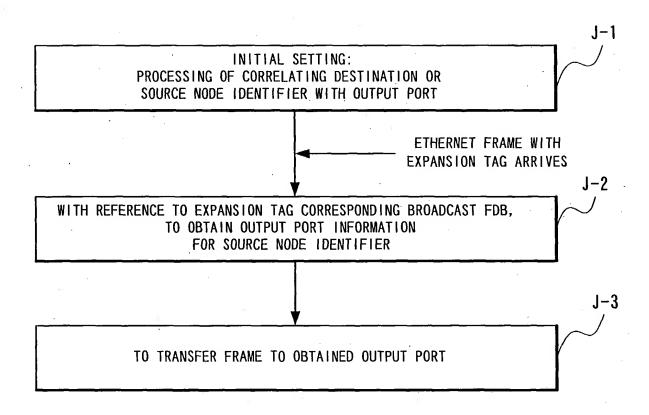


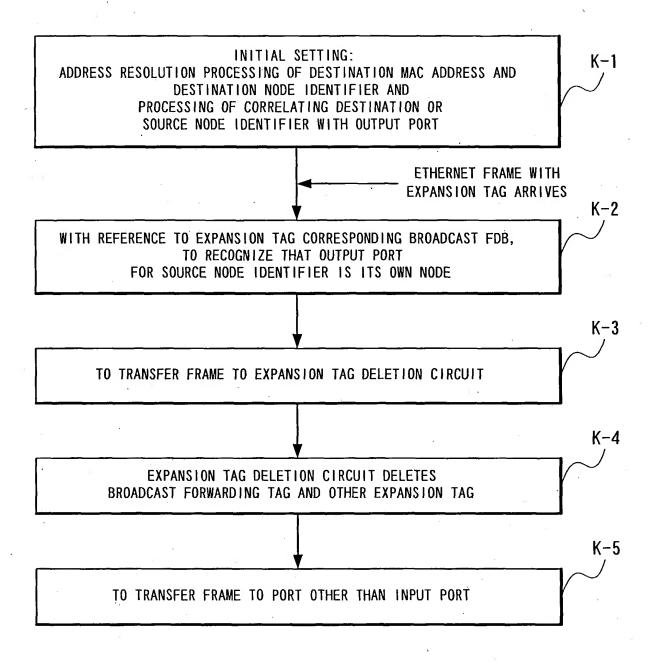


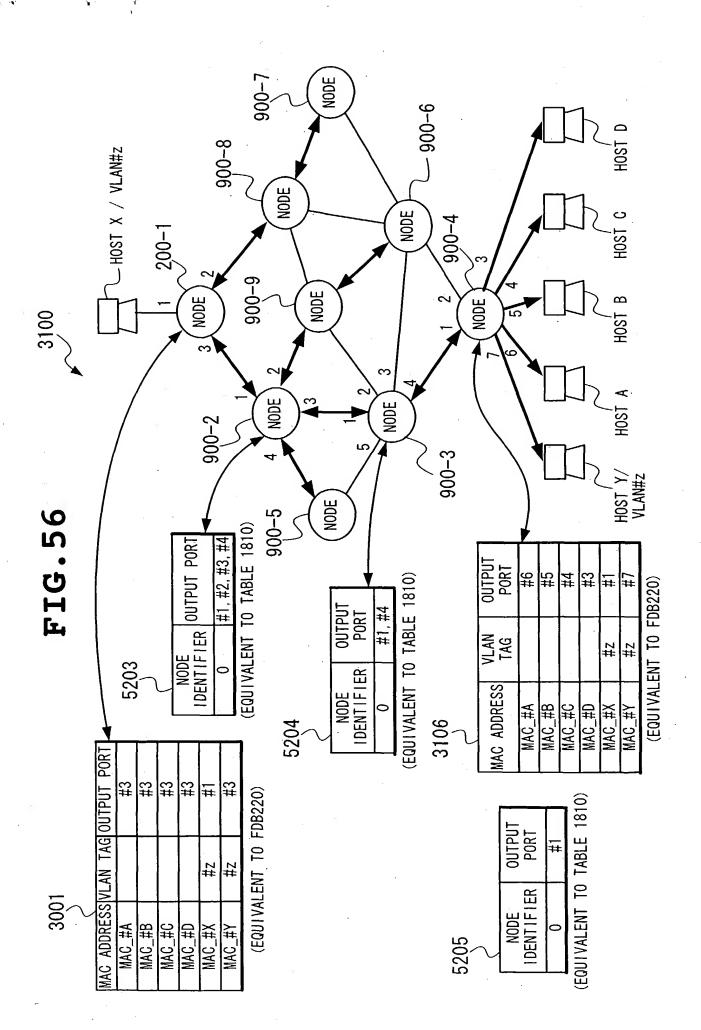


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